

ABSTRACT OF THE DISCLOSURE

A subscriber line interface circuit apparatus includes a signal processor having sense inputs for sensed tip and ring signals of a subscriber loop. The signal processor generates linefeed driver control signals in response to the sensed signals. The signal processor resides on an integrated circuit die. In another embodiment, a subscriber line interface circuit apparatus includes a signal processor generating subscriber loop control signals in response to sensed tip and ring signals from the subscriber loop. A linefeed driver portion drives the subscriber loop in accordance with the subscriber loop control signals. The linefeed driver portion provides the sensed tip and ring signals. Each of the linefeed driver portion and the signal processor resides on an integrated circuit die. In one packaging implementation, the signal processor and the linefeed driver portion reside on separate integrated circuit die within separate integrated circuit packages. In another packaging implementation, the signal processor and linefeed driver portion reside on separate integrated circuit die within the same integrated circuit package. In yet another packaging implementation, the signal processor and the linefeed driver portion reside on the same integrated circuit die. Regardless of packaging, the common mode and differential mode components are calculated by the signal processor rather than the linefeed driver.